REMARKS

In response to the Final Office Action mailed June 29, 2004, and the Advisory Action mailed November 30, 2004, the Applicant respectfully requests reconsideration.

By this amendment, Applicant amends claims 1-7, 9 and 11 solely for clarification (many of which were made solely to provide proper antecedent basis), and adds claims 14-20. As a result, claims 1-20 remain pending for examination, of which claims 1, 9 and 14 are independent.

1.   Telephone Interview

Applicants and Applicants' representatives appreciate the courtesy of Examiner McCarthy in granting and conducting a telephone interview on December 8, 2004, between Examiner McCarthy and Applicants' representative, Daniel P. McLoughlin. The substance of the telephone interview is fully summarized below in Section 3.

2.   Claims 1-8 Patentably Distinguish Over Johnson

Claims 1-3 and 5-8 stand rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,173,421 (Johnson). Applicant respectfully traverses this rejection.

The discussion of Johnson from Applicant's previous response mailed on October 29, 2004, is hereby incorporated by reference.

Claim 1 has been amended as shown above to clarify that the vector recited in claim 1 is a vector *address*. This amendment is supported throughout the specification, for example, on page 6, lines 11-15.

Claim 1 as amended patentably distinguishes over Johnson because Johnson does not teach or suggest a method of debugging a target system using a host system connected thereto, the target system comprising, *inter alia*, a digital signal processor having associated memory and a reserved storage location designated as a *vector address*, the method comprising, *inter alia,* dynamically loading a stack to a reserved region of said memory and causing the *vector address* of said target system to point to said stack, as recited in claim 1. The call stack 55 described in Johnson, and relied upon in the Final Office Action (Page 2, Section 2), is not a vector address. Rather, as is made clear in Johnson, and as one of skill in the art would readily recognize, stack 55 is a complex software construct including code and data representing instances of objects. The software construct includes pointers (i.e., exception or error-handling macros) that help

reduce the amount of code and data on the stack (Col. 10, lines 19-21, 53-62). The &Object1 pointer 230a of call stack 55 is an example of such a macro. (Fig. 2; col. 10, lines 53-55). Such a stack is not an address serving as a reserved storage location. Thus, Johnson does not teach or suggest causing a vector address to point to a stack. Rather, Johnson describes causing error-handling software macros (i.e., pointers) of a software construct (call stack 55) to point to a stack.

In view of the foregoing, claim 1 patentably distinguishes over Johnson. Accordingly, Applicant respectfully requests that the rejection of claim 1 under §102(e) be withdrawn. Claims 2-8 each depend from claim 1 and are patentable for at the same reasons. Accordingly, Applicant respectfully requests that the rejections of claims 2, 3 and 5-8 under §102(e) be withdrawn.

3.   Claims 9-13 Patentably Distinguish Over Johnson

Claims 9-13 stand rejected under 35 U.S.C. § 102(e) as purportedly being anticipated by Johnson. Applicant respectfully traverses this rejection.

Claim 9 has been amended as shown above to clarify that the vector recited in claim 1 is a vector *address*. This amendment is supported throughout the specification, for example, on page 6, lines 11-15.

Claim 9 patentably distinguishes over Johnson because Johnson does not teach or suggest all of the limitations recited in claim 1. Specifically, as should be clear from the discussion of Johnson set forth in Section 2 above, Johnson does not teach or suggest a device for debugging a target system, the target system comprising, *inter alia*, a digital signal processor having associated memory comprising a plurality of addressable locations, said target system further having a reserved storage location designated as a *vector address*, the device further comprising, *inter alia*, stack dynamic loading circuitry for dynamically loading a stack to a reserved region of said memory, whereby said loading circuitry comprises an indication of the location in said memory of said stack, and vector writing circuitry receiving said indication, and writing to said *vector address* the address of said stack, as recited in claim 9.

In view of the foregoing, claim 9 patentably distinguishes over Johnson. Accordingly, Applicant respectfully requests that the rejection of claim under §102(e) be withdrawn. Claims

10-13 each depend from claim 9 and are patentable for at least the same reasons. Accordingly, Applicant respectfully requests that the rejections of these claims under §102(e) be withdrawn.

3. New Claims 14-20 Patentably Distinguish Over Johnson

During the telephone interview, no agreement was reached between Examiner McCarthy and Applicant over Applicant's position that Johnson does not teach or suggest dynamically loading a stack to a reserved region of said memory. However, agreement was reached that claim 1 would distinguish over Johnson if claim 1 were amended to include elements of claims 2 and 4 relating to dynamically loading a library from the host system to the target system, the library including a communication routine enabling the host and target systems to communicate.

Accordingly, Applicant has added new claim 14 as shown above. Claim 14 patentably distinguishes over Johnson because Johnson does not teach or suggest a method of debugging a target system using a host system connected thereto, the method comprising, *inter alia*, dynamically loading a library from the host system to the target system, the library including a communication routine enabling the host system and the target system to communicate, as recited in claim 14.

In view of the foregoing, claim 14 patentably distinguishes over Johnson. Claims 15-20 depend from claim 14 and are patentable for at least the same reasons.
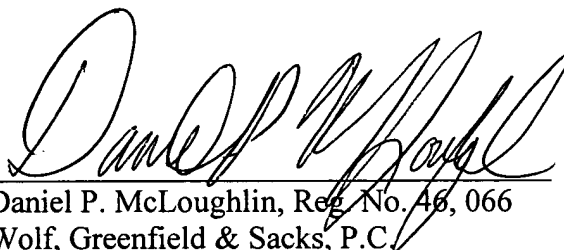
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CONCLUSION

In view of the foregoing amendments and remarks, this application should now be in condition for allowance. A notice to this effect is respectfully requested. If the Examiner believes, after this amendment, that the application is not in condition for allowance, the Examiner is requested to call the Applicant's attorney at the telephone number listed below.

If this response is not considered timely filed and if a request for an extension of time is otherwise absent, Applicant hereby requests any necessary extension of time. If there is a fee occasioned by this response, including an extension fee that is not covered by an enclosed check, please charge any deficiency to Deposit Account No. 23/2825.

Respectfully submitted,

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By: _____

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